

## WEST Search History





DATE: Monday, July 11, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L30	L17 and (current same source)	14
<input type="checkbox"/>	L29	L17 and (current adj source)	1
<input type="checkbox"/>	L28	L24 and (capacitance or inductance)	2
<input type="checkbox"/>	L27	L26 and (capacitance or inductance)	1
<input type="checkbox"/>	L26	L24 and (current same source)	66
<input type="checkbox"/>	L25	L24 and (current adj source)	1
<input type="checkbox"/>	L24	L23 and partition\$ and window	108
<input type="checkbox"/>	L23	(i/o or (input adj output)) same based same simulation	762
<input type="checkbox"/>	L22	L20 and (current or voltage)	14
<input type="checkbox"/>	L21	L20 and (capacitance or inductance)	1
<input type="checkbox"/>	L20	L19 and (i/o or (input adj output))	14
<input type="checkbox"/>	L19	L17 and partition\$	14
<input type="checkbox"/>	L18	L17 and partition\$ and (current adj source)	1
<input type="checkbox"/>	L17	simulation adj window\$	106
<input type="checkbox"/>	L16	L15 and rate	17
<input type="checkbox"/>	L15	L11 and (current adj source)	35
<input type="checkbox"/>	L14	l11 and L13	3
<input type="checkbox"/>	L13	partition\$ same (i/o or (input adj output))	4917
<input type="checkbox"/>	L12	L11 and partition\$	28
<input type="checkbox"/>	L11	simulation same window same (current or voltage)	297
<input type="checkbox"/>	L10	simulation same window same (current adj source)	3
<input type="checkbox"/>	L9	L8 and (circuit same equivalent)	63
<input type="checkbox"/>	L8	L3 and (inductance or capacitance) and (current same based)	108
<input type="checkbox"/>	L7	L5 not l6	4
<input type="checkbox"/>	L6	L5 and characteristic	57
<input type="checkbox"/>	L5	L4 and inductance	61
<input type="checkbox"/>	L4	L3 and (circuit same equivalent)	74
<input type="checkbox"/>	L3	L2 and window	140
<input type="checkbox"/>	L2	partition\$ and (i/o or (input adj output)) and (current adj source) and simulation	329
<input type="checkbox"/>	L1	partion\$ and (i/o or (input adj output)) and (current adj source) and simulation	0

END OF SEARCH HISTORY